

Notice of References Cited	Application/Control No. 10/689,592	Applicant(s)/Patent Under Reexamination TANAKA, GENICHI	
	Examiner Chun-Kuan (Mike) Lee	Art Unit 2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Design Compiler Technology Backgrounder", May 2002, http://web.archive.org/web/20021226010428/http://www.synopsys.com/products/logic/design_comp_tb.pdf
	V	"DC Ultra Technology Backgrounder", May 2002, http://web.archive.org/web/20020820183031/www.synopsys.com/products/logic/dc_ultra_tb_A4.pdf
	W	"Design Budgeting", August 14, 2002, http://web.archive.org/web/20020814203544/http://www.synopsys.com/products/logic/design_budget_wp.pdf
	X	"Design Compiler Family: Product Features", August 14, 2002 http://web.archive.org/web/20020814203544/http://www.synopsys.com/products/logic/features.html

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